

11002 U.S. PTO
10/059896



523
524
525

P

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

BEST AVAILABLE COPY

APPL NUM 10059896	FILING DATE 01/28/2002	CLASS 257	SUBCLASS 621	GAU 2811	EXAMINER Hung VU
**APPLICANTS: Siniaguine Cleg;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CIP OF 09/466,535 12/17/1999 WHICH IS A DIV OF 09/083,927 05/22/1998 PAT 6,184,050 WHICH IS A CON OF PCT/US97/18979 10/27/1997 WHICH CLAIMS BENEFIT OF 60/030,425 10/29/1996					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO M-4532-8P US	
TITLE : Integrated circuits and methods for their fabrication					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING
Amount Due	Date Paid		Sheets Drwg. Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)